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 File 34: SciSearch(R) Cited Ref Sci 1990-2003/Feb W3
 (c) 2003 Inst for Sci Info
 File 99: Wilson Appl. Sci & Tech Abs 1983-2003/Jan
 (c) 2003 The HW Wilson Co.
 File 583: Gale Group Globalbase(TM) 1986-2002/Dec 13
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Set	Items	Description
S1	431	SUBSCRIBER?()LINE?()INTERFACE?()CIRCUIT?
S2	540936	INTEGRATED()CIRCUIT? OR IC OR (PC OR PCMCIA OR MEMORY OR R-OM OR RAM OR CHIP OR PLUGIN)(2N)CARD? OR INSERTION()BOARD? OR ASIC OR DISCRETE()COMPONENT?
S3	1525114	SEMICONDUCTOR? OR CMOS OR NMOS OR PMOS OR (COMPLEMENTARY OR POSITIVE()CHANNEL OR NEGATIVE()CHANNEL)()METAL()OXIDE()SEMICONDUCTOR?
S4	2038763	TIP OR SEND? OR DISPATCH? OR TRANSMIT? OR TRANSMISS?
S5	2195689	RING OR RETURN? OR RECEIV?
S6	11867983	SENSE? OR SENSOR? OR SENSING OR DETECT? OR RECOGNI? OR ISOLAT? OR CALCULAT? OR IDENTIF? OR UNCOVER? OR DIAGNOS?
S7	10712369	ADJUST? OR ACCLIMAT? OR ACCOMODAT? OR ADAPT? OR CONFORM? OR TAILOR? OR MODIF? OR ALTER? OR CUSTOMI? OR READJUST? OR CONTROL?
S8	9225602	CURRENT? OR VOLT? OR POWER? OR ELECTRIC? OR SIGNAL?
S9	1100	LINEFEED? OR LINE()FEED?
S10	86	S1 AND S2 AND S3
S11	254	S1 AND (S2 OR S3)
S12	15	S11 AND S4 AND S5
S13	10	S12 NOT S10
S14	440271	S6(3N)S8
S15	487252	S7(3N)S8
S16	73	S1 AND (S14 OR S15)
S17	9	S16 AND S9
S18	9	S17 NOT (S13 NOT S10)
S19	5	S16 AND S4 AND S5
S20	1	S19 NOT (S18 OR S13 OR S10)
S21	0	S20 AND (S2 OR S3)
S22	3058	S2 AND S3 AND S4 AND S5
S23	417	S22 AND (S14 OR S15)
S24	1	S23 AND S9
S25	186324	S2 AND S3
S26	64947	S4(5N)S5

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S27	5	S10 AND S4 AND S5
S28	3	RD (unique items)
S29	186324	S2 AND S3
S30	64947	S4(5N)S5
S31	1611	S29 AND S30
S32	226	S31 AND (S14 OR S15)
S33	1	S32 AND S9
S34	534	S29(10N)S30
S35	72	S34 AND (S14 OR S15)
S36	56	RD (unique items)
S37	26	S36 AND PY=1999:2003
S38	30	S36 NOT S37

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28/3,K/1 (Item 1 from file: 8)
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04827272 E.I. No: EIP97093842034

Title: Proceedings of the 1996 18th Annual IEEE GaAs IC Symposium

Author: Muller, J.E. (Ed.)

Conference Title: Proceedings of the 1996 18th Annual IEEE GaAs IC Symposium

Conference Location: Orlando, FL, USA Conference Date: 19961103-19961106

E.I. Conference No.: 47009

Source: IEEE Journal of Solid-State Circuits v 32 n 9 Sep 1997. IEEE, Piscataway, NJ, USA. p 1307-1409

Publication Year: 1997

CODEN: IJSCBC ISSN: 0018-9200

Language: English

Title: Proceedings of the 1996 18th Annual IEEE GaAs IC Symposium

...Abstract: Journal of Solid-State Circuits. Topics discussed include: wireless communication amplifiers; power amplifiers; monolithic microwave **integrated circuits** (MMIC); pseudomorphic high electron mobility transistors (PHEMT); automotive radar systems; flip-flop circuits; optical data **receivers**; digital cellular base stations; multifunction macro synthesizer chip (MMSC); Gilbert mixers; personal handy phone (PHS) **transmitters**; low-noise amplifier design; spiral inductors; digital to analog (D/A) converters; analog to digital (A/D) converters; and **subscriber line interface circuit** (SILC).

Descriptors: Solid state devices; Power amplifiers; Monolithic microwave **integrated circuits**; High electron mobility transistors; Radar systems; Mixer circuits; Phase locked loops; **CMOS integrated circuits**; Variable frequency oscillators; Analog to digital conversion

Identifiers: Wireless communication amplifiers; Pseudomorphic high electron mobility transistors (PHEMT); Gilbert mixers; Personal handy phone (PHS) **transmitters**; Multifunction macro synthesizer chip (MMSC); Digital cellular base stations; Optical data **receivers**; Spiral inductors; **Subscriber line interface circuit** (SILC); EiRev

28/3,K/2 (Item 2 from file: 8)
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02076542 E.I. Monthly No: EIM8603-013906

Title: MONOLITHIC 70 V SUBSCRIBER LINE INTERFACE CIRCUIT .

Author: Pieters, Jozef F.; Moons, Elve; Willocx, Eddie; Beckers, Stephan

Corporate Source: Bell Telephone Manufacturing Co, Antwerp, Belg

Conference Title: Proceedings of the IEEE 1985 Custom Integrated Circuits Conference.

Conference Location: Portland, OR, USA Conference Date: 19850520

E.I. Conference No.: 07654

Source: Proceedings of the Custom Integrated Circuits Conference 1985.

Publ by IEEE, New York, NY, USA Available from IEEE Service Cent (Cat n 85CH2157-6), Piscataway, NJ, USA p 101-104

Publication Year: 1985

CODEN: PCICER

Language: English

Title: MONOLITHIC 70 V SUBSCRIBER LINE INTERFACE CIRCUIT .

...Abstract: to the subscriber line, is presented. It is the basic analog segment of a new **subscriber line interface circuit** (SLIC). The LSI chip has been designed using a 70 V BIMOS process, combining high- and low-voltage bipolar transistors (70 V and 15 V) with **CMOS** (15 V) transistors all using the same junction depths. The LSI chip performs nine basic...

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...dc termination impedance; (d) performs conversion from single-ended to balanced line driving for the **received** voice signal and from balanced to single-ended line driving for the **transmit** signal; (e) voltage sensing across the feed resistors; (f) line supervision; (g) overpower detection and...

...Descriptors: Computer Interfaces; **INTEGRATED CIRCUITS** , MONOLITHIC
...

...Design; **INTEGRATED CIRCUITS** , LSI; TRANSISTORS, BIPOLAR; TELEPHONE LINES...

Identifiers: BIMOS LINE INTERFACE CIRCUIT (BLIC) LSI; **SUBSCRIBER LINE INTERFACE CIRCUIT** (SLIC); LINE SUPERVISION; LINE BIASING AND POLARITY

28/3,K/3 (Item 1 from file: 2)

DIALOG(R)File 2:INSPEC

(c) 2003 Institution of Electrical Engineers. All rts. reserv.

03317131 INSPEC Abstract Number: B89018575

Title: A CMOS **subscriber line audio processing circuit including adaptive balance**

Author(s): Poole, S.J.; Surace, G.; Singh, B.; Dyer, N.P.

Author Affiliation: Plessey Co. plc., Swindon, UK

Conference Title: 1988 IEEE International Symposium on Circuits and Systems. Proceedings (Cat. No.88CH2458-8) p.1931-4 vol.2

Publisher: IEEE, New York, NY, USA

Publication Date: 1988 Country of Publication: USA 3 vol. 2915 pp.

U.S. Copyright Clearance Center Code: CH2458-8/88/0000-1931\$01.00

Conference Sponsor: IEEE

Conference Date: 7-9 June 1988 Conference Location: Espoo, Finland

Language: English

Subfile: B

Title: A CMOS **subscriber line audio processing circuit including adaptive balance**

Abstract: The design of a CMOS subscriber-line audio processing circuit including self-adaptive trans-hybrid balance has been described. Its **transmission** performance has been shown to exceed CCITT specifications with up to 12 dB of **transmit** gain and **receive** loss. This has been achieved using a single 5-V power supply while maintaining simple analog interfaces to a **subscriber line interface circuit**. This circuit will enable the design of programmable and flexible line cards, superior **transmission** performance over those realized with more conventional components.

...Descriptors: CMOS integrated circuits ;

Identifiers: CMOS circuit...

... **transmission** performance...

... **transmit** gain...

... **receive** loss

?

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18/3,K/1 (Item 1 from file: 8)
DIALOG(R)File 8:Ei Compendex(R)
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04827296 E.I. No: EIP97093842436

Title: 150-V subscriber line interface circuit (SLIC) in a new BiCMOS/DMOS-technology

Author: Zojer, Bernhard; Koban, Rudiger; Petschacher, Reinhard; Sereinig, Wolfgang

Corporate Source: Siemens Microelectronic Design Cent, Villach, Austria

Conference Title: Proceedings of the 1996 18th Annual IEEE GaAs IC Symposium

Conference Location: Orlando, FL, USA Conference Date: 19961103-19961106

E.I. Conference No.: 47009

Source: IEEE Journal of Solid-State Circuits v 32 n 9 Sep 1997. p 1475-1480

Publication Year: 1997

CODEN: IJSCBC ISSN: 0018-9200

Language: English

Title: 150-V subscriber line interface circuit (SLIC) in a new BiCMOS/DMOS-technology

...Abstract: office subscriber line interface without the need for any transformers or relays. The challenges of subscriber line interface circuit (SLIC) integration stem from the combination of conflicting requirements: low impedance line feeding in a 150-V range, current sensing with 0.2% relative accuracy, and stability up to 200 nF loads, while operating in...

Identifiers: Subscriber line interface circuits (SLIC); Smart power technology (SPT)

18/3,K/2 (Item 1 from file: 2)
DIALOG(R)File 2:INSPEC
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5693574 INSPEC Abstract Number: B9710-6220B-024

Title: A 150-V subscriber line interface circuit (SLIC) in a new BiCMOS/DMOS-technology

Author(s): Zojer, B.; Koban, R.; Petschacher, R.; Sereinig, W.

Author Affiliation: Siemens Microelectron. Design Center, Vllach, Austria

Journal: IEEE Journal of Solid-State Circuits Conference Title: IEEE J. Solid-State Circuits (USA) vol.32, no.9 p.1475-80

Publisher: IEEE,

Publication Date: Sept. 1997 Country of Publication: USA

CODEN: IJSCBC ISSN: 0018-9200

SICI: 0018-9200(199709)32:9L:1475:SLIC;1-6

Material Identity Number: I022-97010

U.S. Copyright Clearance Center Code: 0018-9200/97/\$10.00

Conference Title: 1996 BIPOLAR/BiCMOS Circuits and Technology Meeting

Conference Sponsor: IEEE Electron Devices Soc.; IEEE Circuits & Syst. Soc.; IEEE Twin Cities Sect

Conference Date: 29 Sept.-1 Oct. 1996 Conference Location: Minneapolis, MN, USA

Language: English

Subfile: B

Copyright 1997, IEE

Title: A 150-V subscriber line interface circuit (SLIC) in a new BiCMOS/DMOS-technology

...Abstract: office subscriber line interface without the need for any transformers or relays. The challenges of subscriber line interface circuit (SLIC) integration stem from the combination of conflicting requirements: low impedance line feeding in a 150-V range, current sensing with 0.2% relative accuracy, and stability up to 200 nF loads,

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while operating in...

Identifiers: **subscriber line interface circuit ; ...**

...low impedance line feeding ; ...

... current sensing ;

18/3,K/3 (Item 2 from file: 2)

DIALOG(R)File 2:INSPEC

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5634712 INSPEC Abstract Number: B9708-6220B-017

Title: A subscriber line interface circuit (SLIC) in a new 170 V technology

Author(s): Zojer, B.; Koban, R.; Petschacher, R.; Sereinig, W.

Author Affiliation: Siemens Microelectron. Design Center, Villach, Austria

Journal: Informacije MIDE M vol.27, no.1 p.3-7

Publisher: Soc. Microelectron. Electron. Components & Mater.-MIDEM,

Publication Date: March 1997 Country of Publication: Slovenia

CODEN: IMIDEN ISSN: 0352-9045

SICI: 0352-9045(199703)27:1L.3:SLIC;1-Y

Material Identity Number: N527-97002

Language: English

Subfile: B

Copyright 1997, IEE

Title: A subscriber line interface circuit (SLIC) in a new 170 V technology

...Abstract: relays. The challenges of SLIC integration stem from the combination of conflicting requirements: low impedance **line feeding** in a 150 V range, **current sensing** with 0.2% relative accuracy and stability up to 200 nF loads, while operating in...

Identifiers: **subscriber line interface circuit ; ...**

...low impedance line feeding ; ...

... current sensing ;

18/3,K/4 (Item 3 from file: 2)

DIALOG(R)File 2:INSPEC

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03083569 INSPEC Abstract Number: B88021828

Title: A SLIC with a ringing function

Author(s): Ferrari, R.

Journal: Elettronica Oggi no.51 p.71-2, 74

Publication Date: 30 Nov. 1987 Country of Publication: Italy

CODEN: ELOGDA ISSN: 0391-6391

Language: Italian

Subfile: B

Abstract: The latest **subscriber line interface circuit (SLIC)** comprises two chips, L3000 and the L3010 control unit the interface unit L3000 facilitates the following functions: **line feeding ; line current sensing** and ringing injection. The line control unit L3010 is a signal processor, while the unit...

...Identifiers: subscriber line interface circuit ; ...

... line feeding ; ...

...line current sensing ;

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18/3,K/5 (Item 4 from file: 2)
DIALOG(R)File 2:INSPEC
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02712349 INSPEC Abstract Number: B86049355

Title: A reliable SLIC kit: the SGS alternative

Author(s): Ugge, A.

Author Affiliation: SGS Semicond. Corp., Phoenix, AZ, USA

Conference Title: ELECTRO/85. Conference Record p.9/2/1-8

Publisher: Electron. Conventions Manage, Los Angeles, CA, USA

Publication Date: 1985 Country of Publication: USA 698 pp.

Conference Sponsor: IEEE; METSAC; ERA

Conference Date: 23-25 April 1985 Conference Location: New York, NY, USA

Language: English

Subfile: B

Abstract: After several years of development, SGS is now introducing an advanced and industrially producible **subscriber line interface circuit** (SLIC) kit composed of two ICs. These perform the BORSCH functions (battery feed, our **voltage** protection, ringing **signalling**, **control**, and hybrid) with the support of only a few external passive components and a specially...

...protection IC (L3100). The kit has a high voltage line interface (L3000) and a low **voltage control** unit (L3010) incorporating analog and digital functions. The L3000 provides **line feed**, off-hook detection, line polarity reversal, injection on the line of billing pulses and high-**voltage ringing detection**. The L3010 provides a 2 Mbit/s serial interface to dialog with the line card...

...Identifiers: **subscriber line interface circuit** ; ...

...low **voltage control** unit...

... **line feed** ; ...

...high- **voltage** ringing **detection** ;

18/3,K/6 (Item 1 from file: 144)
DIALOG(R)File 144:Pascal
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13197924 PASCAL No.: 97-0462571

150-V subscriber line interface circuit (SLIC) in a new BiCMOS/DMOS-technology

ZOJER B; KOBAN R; PETSCHACHER R; SEREINIG W

Siemens Microelectronic Design Cent, Villach, Austria

Proceedings of the 1996 18th Annual IEEE GaAs IC Symposium (Orlando, FL, USA) 1996-11-03/1996-11-06

Journal: IEEE Journal of Solid-State Circuits, 1997, 32 (9) 1475-1480

Language: English

150-V subscriber line interface circuit (SLIC) in a new BiCMOS/DMOS-technology

...office subscriber line interface without the need for any transformers or relays. The challenges of **subscriber line interface circuit** (SLIC) integration stem from the combination of conflicting requirements: low impedance **line feeding** in a 150-V range, **current sensing** with 0.2% relative accuracy, and stability up to 200 nF loads, while operating in...

English Descriptors: **Subscriber line interface circuits** (SLIC);
Smart power technology (SPT); Theory; CMOS integrated circuits; Bipolar

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integrated circuits; MOSFET devices; Power...

18/3,K/7 (Item 1 from file: 34)
DIALOG(R)File 34:SciSearch(R) Cited Ref Sci
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06061549 Genuine Article#: XT137 No. References: 6
Title: A 150-V subscriber line interface circuit (SLIC) in a new BiCMOS/DMOS-technology
Author(s): Zojer B (REPRINT) ; Koban R; Petschacher R; Sereinig W
Corporate Source: SIEMENS MICROELECT DESIGN CTR,/A-9500 VILLACH//AUSTRIA/ (REPRINT)
Journal: IEEE JOURNAL OF SOLID-STATE CIRCUITS, 1997, V32, N9 (SEP), P 1475-1480
ISSN: 0018-9200 Publication date: 19970900
Publisher: IEEE-INST ELECTRICAL ELECTRONICS ENGINEERS INC, 345 E 47TH ST, NEW YORK, NY 10017-2394
Language: English Document Type: ARTICLE (ABSTRACT AVAILABLE)

Title: A 150-V subscriber line interface circuit (SLIC) in a new BiCMOS/DMOS-technology
...Abstract: office subscriber line interface without the need for any transformers or relays, The challenges of **subscriber line interface circuit (SLIC)** integration stem from the combination of conflicting requirements: low impedance **line feeding** in a 150-V range, **current sensing** with 0.2% relative accuracy, and stability up to 200 nF loads, while operating in...

18/3,K/8 (Item 2 from file: 34)
DIALOG(R)File 34:SciSearch(R) Cited Ref Sci
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05838662 Genuine Article#: XB020 No. References: 4
Title: A subscriber line interface circuit (SLIC) in a new 170V technology
Author(s): Zojer B; Koban R; Petschacher R; Sereinig W
Corporate Source: SIEMENS AG,MICROELECT DESIGN CTR/A-9500 VILLACH//AUSTRIA/
Journal: INFORMACIJE MIDEM-JOURNAL OF MICROELECTRONICS ELECTRONIC COMPONENTS AND MATERIALS, 1997, V27, N1 (MAR), P3-7
ISSN: 0352-9045 Publication date: 19970300
Publisher: SOC MICROELECTRONICS, ELECTRON COMPONENTS MATERIALS-MIDEM, DUNAJSKA 10, LJUBLJANA, SLOVENIA 61000
Language: English Document Type: ARTICLE (ABSTRACT AVAILABLE)

Title: A subscriber line interface circuit (SLIC) in a new 170V technology
...Abstract: relays. The challenges of SLIC integration stem from the combination of conflicting requirements: low impedance **line feeding** in a 150V range, **current sensing** with 0.2% relative accuracy and stability up to 200nF loads, while operating in the...
...

18/3,K/9 (Item 1 from file: 583)
DIALOG(R)File 583:Gale Group Globalbase(TM)
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03616278
PLESSEY INTRODUCES SLIC FOR TELECOMS
UK - PLESSEY INTRODUCES SLIC FOR TELECOMS
Electronics Weekly (ECW) 25 July 1990 p20
ISSN: 0013-5224

February 21, 2003

Plessey Semiconductor has introduced the SL7950 **subscriber line interface circuit** (SLIC) offering power feed, voice **signal** and **controls** ringing transmission. The SLIC is used at the end of a telephone line in PABX...

... with an analogue line audio processor. The SLIC includes DC control, relay driver, control logic, **line feed** regulator and transceiver.

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20/3,K/1 (Item 1 from file: 2)

DIALOG(R)File 2:INSPEC

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7160826 INSPEC Abstract Number: B2002-02-6215-022

Title: Comparison of power supplies for residential telephony systems

Author(s): Zimnik, M.; Kollman, R.; Betten, J.

Author Affiliation: Texas Instruments Deutschland GmbH, Germany

Conference Title: Twenty-Third International Telecommunications Energy
Conference. INTELEC 2001 (IEE Conf. Publ. No.484) p.540-7

Publisher: IEE, London, UK

Publication Date: 2001 Country of Publication: UK xix+682 pp.

ISBN: 0 85296 744 6 Material Identity Number: XX-2000-02775

Conference Title: Twenty-Third International Telecommunications Energy
Conference. INTELEC 2001

Conference Date: 14-18 Oct. 2001 Conference Location: Edinburgh, UK

Language: English

Subfile: B

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Abstract: With the advent of broadband access products, many manufacturers are **controlling** and **powering** small phone systems. Numerous companies have developed high voltage **subscriber line interface circuits** (SLIC), which control the **ring** and voice **transmission** for these phone systems. This paper presents a comparison of approaches to providing high voltage...

... presented; a low power residential application, a higher power residential application and a single line **ring** generator which removes functions off the SLIC and transfers it into the DC-AC inverter...

...Identifiers: high voltage **subscriber line interface circuits** ;
...

...voice **transmission** ; ...

... **ring** control...

...single line **ring** generator

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33/3,K/1 (Item 1 from file: 2)

DIALOG(R)File 2:INSPEC

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03764576 INSPEC Abstract Number: B90077570

Title: Active phased array antenna concept for ground- and ship-based multifunction radar in X- and C-band

Author(s): Moschuring, H.; Brunner, A.; Kress, E.

Author Affiliation: Siemens AG, Munich, West Germany

Conference Title: Conference Proceedings. Military Microwaves '90 p. 500-5

Publisher: Microwave Exhibitions & Publishers, Tunbridge Wells, UK

Publication Date: 1990 Country of Publication: UK 564 pp.

ISBN: 0 946821 96 8

Conference Date: 11-13 July 1990 Conference Location: London, UK

Language: English

Subfile: B

Abstract: The authors describe active **transmit / receive** modules for future multifunctional phased array radar antennas in the X- and C-band, including microwave monolithic **integrated circuits** in GaAs technology (MMICs) for amplifying or attenuating, switching and phase shifting, **integrated circuits** in **CMOS** -technology for control purposes and the required hybrid circuits especially the internal digitally **controlled power** supply, the internal capacitive storage, the keying and the level shifting subcircuits. Emphasis has been...

... elements a cylindrical waveguide type array of 2000 to 5000 elements is introduced. The preferable **line feed** system respecting the 4-quadrant monopulse division, can offer further subdivision for multi-jammer nulling

...
...Identifiers: active **transmit / receive** modules...

...microwave monolithic **integrated circuits** ; ...

... **CMOS** -technology...

...internal digitally **controlled power** supply...

... **line feed** system

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38/3,K/1 (Item 1 from file: 8)

DIALOG(R)File 8:EI Compendex(R)

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04609925 E.I. No: EIP97013500009

Title: SDH STM-1 termination IC

Author: Xiaoru, Zhang; Lieguang, Zeng

Corporate Source: Tsinghua Univ, Beijing, China

Conference Title: Proceedings of the 1996 2nd International Conference on ASIC

Conference Location: Shanghai, China Conference Date: 19961021-19961024

E.I. Conference No.: 45949

Source: International Conference on ASIC, Proceedings 1996. Shanghai Scientific and Technological Literature Publishing House, Shanghai, China. p 179-182

Publication Year: 1996

CODEN: 002513

Language: English

Abstract: The STM-1 termination IC **receives** and **transmits** Synchronous Digital Hierarchy (SDH) STM-1 signals. In addition to STM-1 frame generation and...

...and performs payload insertion and recovery. Section and path overheads are extracted and inserted, alarm **signals** are **detected** and reported, and performance monitoring at various layers is also performed. Designed as a basic...

Descriptors: Digital integrated circuits; Data communication systems; **Signal detection** ; Application specific integrated circuits; Logic circuits; CMOS integrated circuits

38/3,K/2 (Item 2 from file: 8)

DIALOG(R)File 8:EI Compendex(R)

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04299632 E.I. No: EIP95122945952

Title: Transmitters, receivers and detectors for high speed parallel optical interconnect links

Author: Levine, B.F.; Tu, K.-Y.; Gabara, T.J.; Wynn, J.D.; Dutta, N.K.; Monteleone, K.

Corporate Source: AT&T Bell Lab, Murray Hill, NJ, USA

Conference Title: Proceedings of the LEOS Summer Topical Meetings

Conference Location: Keystone, CO, USA Conference Date: 19950807-19950811

E.I. Conference No.: 44022

Source: LEOS Summer Topical Meeting 1995. IEEE, Piscataway, NJ, USA. 2pp

Publication Year: 1995

CODEN: 001638

Language: English

Descriptors: Optical interconnects; Optical links; **Transmitters** ; **Signal receivers** ; **Detectors** ^Bandwidt ; Bandwidth; Asynchronous transfer mode; Switches; **Integrated circuits** ; Communication channels (information theory)

38/3,K/3 (Item 3 from file: 8)

DIALOG(R)File 8:EI Compendex(R)

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04204795 E.I. No: EIP95052693126

Title: Parallel optical interconnection for high-speed data links

Author: Levine, Barry F.; Wynn, James D.; Vakhshoori, Daryoosh; Monteleone, K.

February 21, 2003

Corporate Source: AT&T Bell Labs., Livingston, NJ, USA
Conference Title: Optoelectronic Integrated Circuit Materials, Physics,
and Devices

Conference Location: San Jose, CA, USA Conference Date:
19950206-19950209

E.I. Conference No.: 22235

Source: Proceedings of SPIE - The International Society for Optical
Engineering v 2397 1995. Society of Photo-Optical Instrumentation
Engineers, Bellingham, WA, USA. p 2-11

Publication Year: 1995

CODEN: PSISDG ISSN: 0277-786X ISBN: 0-8194-1744-0

Language: English

Descriptors: Optical interconnects; Optoelectronic devices; Bit error
rate; **Detectors** ; **Transmitters** ; **Signal receivers** ; **Arrays**^Mo ;
Monolithic integrated circuits ; Semiconductor devices; Optical links

38/3,K/4 (Item 4 from file: 8)

DIALOG(R)File 8:EI Compendex(R)

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04052233 E.I. No: EIP95012532044

Title: **Development of a photonic integrated transceiver chip for WDM
transmission**

Author: Matz, R.; Bauer, G.; Clemens, P.; Heise, G.; Mahlein, H.F.;
Metzger, W.; Michel, H.; Schulte-Roth, G.

Corporate Source: Corporate Research and Development of Siemens,
Muenchen, Ger

Source: IEEE Photonics Technology Letters 6 11 Nov 1994. p 1327-1329

Publication Year: 1994

CODEN: IPTLEL ISSN: 1041-1135

Language: English

...Abstract: means to reduce module costs. Here we describe a generic
fabrication process for InP photonic **integrated circuits** and
demonstrate an initial transceiver chip with **transmit** , **receive** and
1300/1530 nm wavelength division multiplexing functions. The chip output
power reaches 1 mW...

Identifiers: Photonic integrated transceiver chip; Frequency division
multiplexing transmission; Line terminals; Customer access modules;
Detection efficiency; Laser threshold **current**

38/3,K/5 (Item 5 from file: 8)

DIALOG(R)File 8:EI Compendex(R)

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04042284 E.I. No: EIP94112435745

Title: **Telemetry-instrumentation system for monitoring multiple
subcutaneously implanted glucose sensors**

Author: Shults, M.C.; Rhodes, R.K.; Updike, S.J.; Gilligan, B.J.;
Reining, W.N.

Corporate Source: Univ of Wisconsin Medical Cent, Madison, WI, USA

Source: IEEE Transactions on Biomedical Engineering v 41 n 10 Oct 1994. p
937-942

Publication Year: 1994

CODEN: IEBEAX ISSN: 0018-9294

Language: English

...Abstract: implantable potentiostat-radiotelemetry system for in vivo
sensing of glucose is described. An enzyme electrode **sensor** measures the
oxidation **current** of hydrogen peroxide formed by the stoichiometric
conversion of glucose substrate and oxygen cofactor in an immobilized
glucose oxidase layer. The **sensor current** is converted to a frequency

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and transmitted at programmable intervals (4, 32, 256 s) to...

...each sensor, allowing over 10 sensors within the same 10 m radius. A custom interface **card** allows a **PC** to program the **receiver** and handle the **transmitted** sensor data using software written in Microsoft C and QuickBasic. Software control allows on-the...

38/3,K/6 (Item 6 from file: 8)

DIALOG(R)File 8:Ei Compendex(R)

(c) 2003 Elsevier Eng. Info. Inc. All rts. reserv.

03798709 E.I. No: EIP94021201034

Title: Highly integrated MMIC K-band transmit/receive chip

Author: Fudem, Howard; Moghe, Sanjay; Dietz, Greg; Consolazio, Steve

Corporate Source: Northrop Electronics Systems Div, Rolling Meadows, IL, USA

Conference Title: Proceedings of the 1993 IEEE Microwave and Millimeter-Wave Monolithic Circuits Symposium

Conference Location: Atlanta, GA, USA Conference Date: 19930614-19930615

E.I. Conference No.: 19726

Source: Proc 1993 IEEE Microwave Millimeter Wave Monolithic Circ Symp 1993. Publ by IEEE, IEEE Service Center, Piscataway, NJ, USA, (IEEE cat n 93CH3316-7). p 119-122

Publication Year: 1993

ISBN: 0-7803-1323-2

Language: English

...Abstract: three 3-stage amplifiers, an active power divider, a diode double-balanced mixer, and a **voltage controlled** oscillator (VCO) all integrated on a single chip 96 multiplied by 71 mils (2.4...

Identifiers: Monolithic micrometer **integrated circuits** ; K-band **transmit / receive chip**; **Voltage controlled** oscillator

38/3,K/7 (Item 7 from file: 8)

DIALOG(R)File 8:Ei Compendex(R)

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03790497 E.I. No: EIP94011190775

Title: ASIC chipset for optical communications

Author: Chan, E.S.; Lawrence, L.A.

Corporate Source: Galaxy Microsystems, Inc, San Jose, CA, USA

Conference Title: Proceedings of the 15th Annual IEEE GaAs IC Symposium

Conference Location: San Jose, CA, USA Conference Date: 19931010-19931013

E.I. Conference No.: 19773

Source: Technical Digest - GaAs IC Symposium (Gallium Arsenide Integrated Circuit) 1993. Publ by IEEE, IEEE Service Center, Piscataway, NJ, USA, (IEEE cat n 93CH3346-4). p 303-306

Publication Year: 1993

CODEN: TDGSEE ISBN: 0-7803-1393-3

Language: English

Descriptors: **Integrated circuits** ; Optical communication; **Transmitters** ; Signal **receivers** ; Semiconducting gallium arsenide; Optical links; Pulse modulation; Lasers; **Signal encoding**; **Signal detection**

38/3,K/8 (Item 8 from file: 8)

DIALOG(R)File 8:Ei Compendex(R)

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03058994 E.I. Monthly No: EI9105056907

February 21, 2003

Title: Integrated bipolar 200Mbit/s data regenerator.

Author: Wilson, John

Corporate Source: Swindon Silicon

Source: Electronic Engineering (London) v 62 n 768 Dec 1990 4p

Publication Year: 1990

CODEN: ELEGAP ISSN: 0013-4902

Language: English

Abstract: By completing a phase locked loop (PLL) using an external **voltage controlled** oscillator (VCO) and loop filter, all the necessary functions (amplification, decision and timing recovery circuits...

...out that a complete optical regenerator can be constructed by the addition of an optical **receiver** and **transmitter**. The specifications for the **integrated circuit** regenerator are outlined.

38/3,K/9 (Item 9 from file: 8)

DIALOG(R)File 8:Ei Compendex(R)

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01756873 E.I. Monthly No: EI8505038523 E.I. Yearly No: EI85072975

Title: 1200 BIT/S QPSK FULL DUPLEX MODEM.

Author: Hanson, Kerry; Severin, William A.; Klinkovsky, Edward R.; Richardson, Donald C.; Hochschild, James R.

Corporate Source: Texas Instruments Inc, Data Systems Group, Houston, TX, USA

Source: IEEE Journal of Solid-State Circuits v SC-19 n 6 Dec 1984 p 878-887

Publication Year: 1984

CODEN: IJSCBC ISSN: 0018-9200 ISBN: 0-13-782178-6

Language: ENGLISH

...Abstract: uses charge storage on ratioed capacitors and allows the design of filters, amplifiers, automatic gain **control** elements, **voltage controlled** oscillators, Hilbert filters, phase-locked loops, and a fully adaptive equalizer to be implemented on...

...same integrated circuit as the digital functions, which include the clock generation circuits and the **transmit** and **receive** buffers. Performance measurements show that the IC meets the bit error rate standard at 12 db signal-to-noise ratio. 12 refs.

Identifiers: FULL-DUPLEX VOICE-BAND MODEM; SWITCHED-CAPACITOR **SIGNAL** PROCESSING; **VOLTAGE CONTROLLED OSCILLATORS**

38/3,K/10 (Item 1 from file: 35)

DIALOG(R)File 35:Dissertation Abs Online

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01298870 ORDER NO: NOT AVAILABLE FROM UNIVERSITY MICROFILMS INT'L.

BICMOS PHASE-LOCKED LOOPS FOR SERIAL DATA COMMUNICATION

Author: THOMPSON, BARRY L.

Degree: PH.D.

Year: 1993

Corporate Source/Institution: MASSACHUSETTS INSTITUTE OF TECHNOLOGY (0753)

Source: VOLUME 54/03-B OF DISSERTATION ABSTRACTS INTERNATIONAL. PAGE 1589.

...A master/slave dual-loop architecture sets the center frequency of the receive clock recovery **voltage - controlled** oscillator from the transmit frequency synthesis PLL. This architecture reduces the center frequency offset of the receive **voltage - controlled** oscillator and thus aids in acquisition and eliminates harmonic locking problems.

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Digital functions are implemented...

...speed (300 MHz). This combination of styles allows higher speed operation than is possible with CMOS and lower power dissipation and area than ECL.

Since the transmit and receive circuits operate asynchronously, and since this is a mixed analog/digital design, coupling noise potentially ...

38/3,K/11 (Item 1 from file: 2)

DIALOG(R)File 2:INSPEC

(c) 2003 Institution of Electrical Engineers. All rts. reserv.

5766566 INSPEC Abstract Number: B9801-7210F-001, C9801-3250-002

Title: A new passive CMOS telemetry chip to receive power and transmit data for a wide range of sensor applications

Author(s): Dudenbostel, D.; Krieger, K.-L.; Candler, C.; Laur, R.

Author Affiliation: Inst. for Electromagnetic Theory & Microelectron., Bremen Univ., Germany

Conference Title: Transducers 97. 1997 International Conference on Solid-State Sensors and Actuators. Digest of Technical Papers (Cat. No.97TH8267) Part vol.2 p.995-8 vol.2

Publisher: IEEE, New York, NY, USA

Publication Date: 1997 Country of Publication: USA 2 vol. 1525 pp.

ISBN: 0 7803 3829 4 Material Identity Number: XX97-02070

U.S. Copyright Clearance Center Code: 0 7803 3829 4/97/\$10.00

Conference Title: Proceedings of International Solid State Sensors and Actuators Conference (Transducers '97)

Conference Sponsor: IEEE Electron Devices Soc

Conference Date: 16-19 June 1997 Conference Location: Chicago, IL, USA

Language: English

Subfile: B C

Copyright 1997, IEE

Title: A new passive CMOS telemetry chip to receive power and transmit data for a wide range of sensor applications

...Abstract: possibility of integrated micro coils on the chip is presented. The telemetry chip receives the power for the sensor microsystem. Using a passive modulation technique the telemetry chip transmits measured data to the external...

...Descriptors: electric sensing devices

38/3,K/12 (Item 2 from file: 2)

DIALOG(R)File 2:INSPEC

(c) 2003 Institution of Electrical Engineers. All rts. reserv.

5565684 INSPEC Abstract Number: B9706-6230-003, C9706-5135-004

Title: An SDH STM-1 termination IC

Author(s): Zhang Xiaoru; Zeng Lieguang

Author Affiliation: Dept. of Electron. Eng., Tsinghua Univ., Beijing, China

Conference Title: 1996 2nd International Conference on ASIC Proceedings (IEEE Cat. No.96TH8140) p.179-82

Editor(s): Zhang Qian-Ling; Tang Ting-Ao; Yu Huihua

Publisher: Shanghai Sci. & Technol. Literature Publishing House, Shanghai, China

Publication Date: 1996 Country of Publication: China 452 pp.

Material Identity Number: XX96-03455

Conference Title: Proceedings of 2nd International Conference on ASIC

Conference Sponsor: Chinese Inst. Electron.; IEEE Beijing Sect.; Nat. Natural Sci. Found. China; K.C. Wong Educ. Found., Hong Kong; IEE Electron. Div.; IEEE Circuits & Syst. Soc.; ACM SIGDA

Conference Date: 21-24 Oct. 1996 Conference Location: Shanghai, China

Language: English

February 21, 2003

Subfile: B C

Copyright 1997, IEE

Abstract: The STM-1 termination IC receives and transmits Synchronous Digital Hierarchy (SDH) STM-1 signals. In addition to STM-1 frame generation and...

... and performs payload insertion and recovery. Section and path overheads are extracted and inserted, alarm signals are detected and reported, and performance monitoring at various layers is also performed. Designed as a basic...

...Identifiers: alarm signal detection ;

38/3,K/13 (Item 3 from file: 2)

DIALOG(R)File 2:INSPEC

(c) 2003 Institution of Electrical Engineers. All rts. reserv.

5475265 INSPEC Abstract Number: B9702-6250F-075

Title: A single-chip GaAs RF transceiver for 1.9-GHz digital mobile communication systems

Author(s): Yamamoto, K.; Maemura, K.; Kasai, N.; Yoshii, Y.; Miyazaki, Y.; Nakayama, M.; Ogata, N.; Takagi, T.; Otsubo, M.

Author Affiliation: Optoelectron. & Microwave Devices R&D Lab., Mitsubishi Electr. Corp., Hyogo, Japan

Journal: IEEE Journal of Solid-State Circuits Conference Title: IEEE J. Solid-State Circuits (USA) vol.31, no.12 p.1964-73

Publisher: IEEE,

Publication Date: Dec. 1996 Country of Publication: USA

CODEN: IJSCBC ISSN: 0018-9200

SICI: 0018-9200(199612)31:12L:1964:SCGT;1-E

Material Identity Number: I022-96015

U.S. Copyright Clearance Center Code: 0018-9200/96/\$05.00

Conference Title: 1996 IEEE International Solid-State Circuits Conference

Conference Date: 8-10 Feb. 1996 Conference Location: San Francisco, CA, USA

Language: English

Subfile: B

Copyright 1997, IEE

...Abstract: and high-volume production. This IC includes a negative voltage generator for 3-V single voltage operation and a control logic circuit to control transmit and receive functions, together with RF front-end analog circuits-a power amplifier, an SPDT switch, two attenuators for transmit and receive modes, and a low-noise amplifier. The IC can deliver 22-dBm output power at 30% efficiency with 3-V single power supply...

38/3,K/14 (Item 4 from file: 2)

DIALOG(R)File 2:INSPEC

(c) 2003 Institution of Electrical Engineers. All rts. reserv.

5125039 INSPEC Abstract Number: B9601-6250F-066

Title: A low-distortion GaAs variable attenuator IC for digital mobile communication system

Author(s): Miyatsuji, K.; Ueda, D.

Author Affiliation: Matsushita Electron. Corp., Osaka, Japan

Conference Title: 1995 IEEE International Solid-State Circuits Conference. Digest of Technical Papers. ISSCC (Cat. No.95CH35753) p. 42-3, 338

Editor(s): Wuorinen, J.H.

Publisher: IEEE, New York, NY, USA

Publication Date: 1995 Country of Publication: USA 440 pp.

ISBN: 0 7803 2495 1

U.S. Copyright Clearance Center Code: 0 7803 2495 1/95/\$4.00

Conference Title: Proceedings ISSCC '95 - International Solid-State

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Circuits Conference

Conference Date: 15-17 Feb. 1995 Conference Location: San Francisco, CA, USA

Language: English

Subfile: B

Copyright 1995, IEE

Abstract: This GaAs variable RF attenuator IC shows extremely low distortion with a single positive **control voltage**. The low-distortion characteristic is realized by employing octal-gate MESFET structures as the voltage...

... is lower than -30 dB in the whole attenuation range. The present GaAs variable attenuator IC is suitable for **transmitting receiving power control** for a variety of digital mobile communication systems such as TDMA or CDMA.

38/3,K/15 (Item 5 from file: 2)

DIALOG(R)File 2:INSPEC

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4938977 INSPEC Abstract Number: B9506-1350H-017

Title: High-performance GaAs switch ICs fabricated using MESFETs with two kinds of pinch-off voltages (for handy phone)

Author(s): Uda, H.; Sawai, T.; Yamada, T.; Nogawa, K.; Harada, Y.

Author Affiliation: Sanyo Electric Co., Ltd., Osaka, Japan

p.247-50

Publisher: IEEE, New York, NY, USA

Publication Date: Oct. 1993 Country of Publication: USA xvii+382 pp.

ISBN: 0 7803 1393 3

U.S. Copyright Clearance Center Code: 0-7803-1393-3/93/\$3.00

Conference Title: 15th Annual GaAs IC Symposium

Conference Sponsor: IEEE

Conference Date: 10-13 Oct. 1993 Conference Location: San Jose, CA, USA

Language: English

Subfile: B

Copyright 1995, IEE

Abstract: GaAs MESFET switch ICs operating at low **control voltages** of 0V/-3V and +3V/0V have been developed for use in the personal handy...
...dB and 0.65 dB, and high isolation of 31 dB and 24 dB at **receiving** and **transmitting** operation, respectively. The +3V/0V IC also had excellent characteristics such as insertion loss of 0.73 dB and 0.95...

...Identifiers: low **control voltages** ;

38/3,K/16 (Item 6 from file: 2)

DIALOG(R)File 2:INSPEC

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4797344 INSPEC Abstract Number: B9412-1350H-003

Title: High-performance GaAs switch IC's fabricated using MESFET's with two kinds of pinch-off voltages and a symmetrical pattern configuration

Author(s): Uda, H.; Yamada, T.; Sawai, T.; Nogawa, K.; Harada, Y.

Author Affiliation: Microelectron. Res. Center, Sanyo Electr. Co. Ltd., Osaka, Japan

Journal: IEEE Journal of Solid-State Circuits vol.29, no.10 p. 1262-9

Publication Date: Oct. 1994 Country of Publication: USA

CODEN: IJSCBC ISSN: 0018-9200

U.S. Copyright Clearance Center Code: 0018-9200/94/\$04.00

Language: English

Subfile: B

February 21, 2003

Abstract: GaAs MESFET switch IC's operating at low **control voltages** of 0/-3 V and +3/0 V have been developed for use in Personal...

...dB and 0.65 dB, and high isolation of 31 dB and 24 dB at **receiving and transmitting** operations, respectively. The +3/0 V IC also had excellent characteristics such as insertion loss of 0.73 dB and 0.95...

...Identifiers: **control voltages** ;

38/3,K/17 (Item 7 from file: 2)

DIALOG(R)File 2:INSPEC

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4653457 INSPEC Abstract Number: B9406-2570D-004

Title: **A CMOS circuit for real-time chip temperature measurement**

Author(s): Boyle, S.R.; Heald, R.A.

p.286-91

Publisher: IEEE Comput. Soc. Press, Los Alamitos, CA, USA

Publication Date: 1994 Country of Publication: USA xv+500 pp.

ISBN: 0 8186 5380 9

U.S. Copyright Clearance Center Code: 1063-6390/94/\$3.00

Conference Title: Proceedings of COMPCON '94

Conference Sponsor: IEEE

Conference Date: 28 Feb.-4 March 1994 Conference Location: San Francisco, CA, USA

Language: English

Subfile: B

...Abstract: noise-immune chip temperature sensor circuit has been designed and implemented on Intergraph Clipper C400 **CMOS** chips. The temperature **sensor** is a **current - controlled CMOS ring oscillator** which **sends** out a squarewave signal whose frequency decreases with temperature. The **sensor** and **signal** are relatively immune to the noise of the CMOS chip. The noise immunity allows the...

...Descriptors: **electric sensing devices**

...Identifiers: **current - controlled CMOS ring oscillator**

38/3,K/18 (Item 8 from file: 2)

DIALOG(R)File 2:INSPEC

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4509190 INSPEC Abstract Number: B9312-1350F-004

Title: **Low voltage, high power T/R switch MMIC using LC resonators**

Author(s): Tokumitsu, T.; Toyoda, I.; Aikawa, M.

Author Affiliation: NTT Radio Commun. Syst. Lab., Kanagawa, Japan

Conference Title: IEEE 1993 Microwave and Millimeter-Wave Monolithic Circuits Symposium. Digest of Papers (Cat. No.93CH3316-7) p.27-30

Publisher: IEEE, New York, NY, USA

Publication Date: 1993 Country of Publication: USA 171 pp.

ISBN: 0 7803 1322 4

U.S. Copyright Clearance Center Code: 93CH3316-7/93/0000-0027\$01.00

Conference Sponsor: IEEE

Conference Date: 14-15 June 1993 Conference Location: Atlanta, GA, USA

Language: English

Subfile: B

Abstract: A T/R (**transmit / receive**) MMIC (monolithic microwave **integrated circuit**) switch for high-power/low-distortion operation at low **control voltage** is proposed. LC-resonant switches composed of inductors, capacitors, and switching FETs are incorporated in...

...Identifiers: low **control voltage** ;

38/3,K/19 (Item 9 from file: 2)

February 21, 2003

DIALOG(R)File 2:INSPEC

(c) 2003 Institution of Electrical Engineers. All rts. reserv.

03956894 INSPEC Abstract Number: B91057893

Title: Array backplate architecture for monolithic microwave integrated circuits

Author(s): Shostak, D.R.; Smith, E.C., Jr.; Price, R.A.

Author Affiliation: Rockwell Int. Corp., Anaheim, CA, USA

Conference Title: 1990 International Symposium Digest. Antennas and Propagation. Institute of Electrical and Electronics Engineers. Merging Technologies for the 90's (Cat. No. 90CH2776-3) p.1413-16 vol.4

Publisher: IEEE, New York, NY, USA

Publication Date: 1990 Country of Publication: USA 4 vol. 2072 pp.

U.S. Copyright Clearance Center Code: CH2776-3/90/0000-1413\$01.00

Conference Sponsor: IEEE

Conference Date: 7-11 May 1990 Conference Location: Dallas, TX, USA

Language: English

Subfile: B

...Abstract: manufacture than earlier design approaches. A concept for an array backplate architecture for monolithic microwave **integrated circuit** (MMIC) applications is discussed. This concept for EHF **receive** and **transmit** array backplates relies on proven RF distribution structures and features an advanced fabrication technique which...

... of providing individually fabricated RF feedthroughs and a network of wires for the DC and **control signal** distribution. The backplate is stress-free because the heatsink matches the ceramic. The GaAs MMICs...

38/3,K/20 (Item 10 from file: 2)

DIALOG(R)File 2:INSPEC

(c) 2003 Institution of Electrical Engineers. All rts. reserv.

03174246 INSPEC Abstract Number: B88044366

Title: Low-power MIC diode doubler

Author(s): Hess, R.J.

Author Affiliation: Loral Terracom, San Diego, CA, USA

Journal: MSN Microwave Systems News & Communications Technology
vol.17, no.13 p.58, 60, 62, 64, 67

Publication Date: Dec. 1987 Country of Publication: USA

CODEN: MSNTEC ISSN: 8750-7935

Language: English

Subfile: B

...Abstract: MIC-type circuit is described. The frequency doubler described is used in a hybrid microwave **integrated - circuit** (MIC) RF module in a communications **transmitter** chain, or a **receiver** local oscillator in a 4.4 to 5.0 GHz frequency band. It features: low...

... using a temperature-compensating sensistor; CAD-optimized input, output circuits; low conversion loss; and input **signal isolation**. The circuits are constructed on alumina substrates. Self-bias eases hermeticizing. The simple filters result...

...Identifiers: input **signal isolation** ;

38/3,K/21 (Item 11 from file: 2)

DIALOG(R)File 2:INSPEC

(c) 2003 Institution of Electrical Engineers. All rts. reserv.

02670361 INSPEC Abstract Number: B86035738

Title: ISSCC: special purpose chips

Author(s): Allan, R.

Journal: Electronic Design vol.34, no.4 p.119-23

Publication Date: 20 Feb. 1986 Country of Publication: USA

February 21, 2003

CODEN: ELODAW ISSN: 0013-4872
Language: English
Subfile: B

...Abstract: included discussions about dense and fast ICs for optical communications and image sensing and processing. **CMOS** technology has produced a pair of optical devices, a **transmitter** and **receiver**, capable of asynchronous data rates of 200 and 50 Mbits/s, respectively. An example of...

... CCD imagers. Many of these advances owe their existence to new imager structures. Once video **signals** are **sensed**, digitized, and acquired, they must be processed at very high speeds to be useful. Processors...

38/3,K/22 (Item 12 from file: 2)
DIALOG(R)File 2:INSPEC
(c) 2003 Institution of Electrical Engineers. All rts. reserv.

02144894 INSPEC Abstract Number: B83060140, C83041615
Title: IR remote control using CMOS devices SLB 3801 and SLB 3802
Author(s): Beitner, M.; Vogel, D.
Author Affiliation: Siemens AG, Munchen, West Germany
Journal: Siemens Components (English Edition) vol.18, no.3 p.93-8
Publication Date: June 1983 Country of Publication: West Germany
CODEN: SICOD5 ISSN: 0173-1734
Language: English
Subfile: B C

...Abstract: can be transmitted by IR (infrared) radiation. Integrated circuits SLB 3801 and SLB 3802 in **CMOS** technology have been designed as the **transmitter** and **receiver**, respectively, for noise-immune PCM **transmission** systems allowing remote control with either cable or IR radiation. The ICs described are also...

... controls with and without confirmatory signals; for example, on/off switching of loads in ripple **control** systems in DC **voltage** networks.

38/3,K/23 (Item 1 from file: 94)
DIALOG(R)File 94:JICST-EPlus
(c)2003 Japan Science and Tech Corp(JST). All rts. reserv.

03828188 JICST ACCESSION NUMBER: 98A0927767 FILE SEGMENT: JICST-E
Optical Link Modules for 2.5Gb/s SDH Applications.
TSUMURA EIJI (1); TAKAHASHI SATOSHI (1); FUJIMURA YASUSHI (1); MURATA KAZUO (1); NISHIYAMA NAOKI (1); NISHIE MITSUAKI (1)
(1) Sumitomo Electr. Ind., Ltd.
SEI Tekunikaru Rebyu(Sumitomo Electric Technical Review), 1998, NO.153,
PAGE.20-24, FIG.9, TBL.4, REF.7
JOURNAL NUMBER: F0314AAL CODEN: SUDEA
UNIVERSAL DECIMAL CLASSIFICATION: 621.391.6
LANGUAGE: Japanese COUNTRY OF PUBLICATION: Japan
DOCUMENT TYPE: Journal
ARTICLE TYPE: Original paper
MEDIA TYPE: Printed Publication

...ABSTRACT: Each module contains a dual in-line package(DIP) whose total volume is 17.9cm³(**transmitter** : 4.6cm³, **receiver** 13.3cm³). The authors have developed an LD Driver IC, an APC(Automatic **Power Control**) IC, a Preamplifier IC and a 3R(Reshaping, Retiming, Regenerating) IC in order to reduce...

38/3,K/24 (Item 2 from file: 94)

February 21, 2003

DIALOG(R)File 94:JICST-EPlus
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01407560 JICST ACCESSION NUMBER: 91A0735542 FILE SEGMENT: JICST-E
**A Dual Frequency Synthesizer IC with SAW VCOs for Transmitter / Receiver
using Bi- CMOS Process.**

ADACHI NAOKI (1); MIMURA MASAHIRO (1); TAKAHASHI KAZUAKI (1); OBA MOTOI
(1); HASEGAWA MAKOTO (1); HIGASHIDA YASUSHI (2); SHIMAZU TAKAYUKI (3)
(1) Matsushita Electr. Ind. Co., Ltd.; (2) Matsushita Commun. Ind. Co.,
Ltd.; (3) Matsushita Electron. Corp.

Denshi Joho Tsushin Gakkai Taikai Koen Ronbunshu(Proceedings of the IEICE
General Conference (Institute of Electronics, Information and
Communication Engineers), 1991, VOL.1991,NO.Shuki Pt 2, PAGE.2.206,
FIG.3, TBL.1, REF.2

JOURNAL NUMBER: G0508AEP

UNIVERSAL DECIMAL CLASSIFICATION: 621.374.4 621.382.2/.3.049.77

LANGUAGE: Japanese COUNTRY OF PUBLICATION: Japan

DOCUMENT TYPE: Conference Proceeding

ARTICLE TYPE: Short Communication

MEDIA TYPE: Printed Publication

**A Dual Frequency Synthesizer IC with SAW VCOs for Transmitter / Receiver
using Bi- CMOS Process.**

...DESCRIPTORS: **voltage control ;**

...BROADER DESCRIPTORS: **electric quantity control ;**

38/3,K/25 (Item 3 from file: 94)

DIALOG(R)File 94:JICST-EPlus
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01376508 JICST ACCESSION NUMBER: 91A0357791 FILE SEGMENT: JICST-E
**Transmitting and receiving circuit for ultrasonic diagnostic
equipment. High- voltage IC design.**

SHINOMURA RYUICHI (1); TAKASUGI WASAO (1); MATSUNAGA YOSHIKUNI (1)
(1) Hitachi, Ltd., Central Res. Lab.

Denshi Joho Tsushin Gakkai Zenkoku Taikai Koen Ronbunshu(Spring National
Convention Record, the Institute of Electronics, Information and
Communication Engineers), 1991, VOL.1991,NO.Spring Pt 1, PAGE.1.370,
FIG.4, REF.2

JOURNAL NUMBER: G0508ADY

UNIVERSAL DECIMAL CLASSIFICATION: 620.179:669

LANGUAGE: Japanese COUNTRY OF PUBLICATION: Japan

DOCUMENT TYPE: Conference Proceeding

ARTICLE TYPE: Short Communication

MEDIA TYPE: Printed Publication

**Transmitting and receiving circuit for ultrasonic diagnostic
equipment. High- voltage IC design.**

38/3,K/26 (Item 1 from file: 6)

DIALOG(R)File 6:NTIS
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1900155 NTIS Accession Number: PB95-259412
Anritsu Technical Bulletin, No. 69, March 1995
Anritsu, Inc., Tokyo (Japan).
Corp. Source Codes: 099499000
cMar 95 123p

Languages: Japanese

Journal Announcement: GRAI9521

Text in Japanese with English abstracts. Portions of this document are
not fully legible. See also PB95-178075.

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NTIS Prices: PC E10/MF E10

Descriptors: Communication equipment; **Semiconductor** lasers; Radio stations; Multimedia; Image processing; **Transmitter** receivers; Data processing terminals; Radio **transmission**; Measuring instruments; Network synthesis; **Quality control**; Printers; **Integrated circuits**; **Electric** conductors

38/3,K/27 (Item 2 from file: 6)

DIALOG(R)File 6:NTIS

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1482753 NTIS Accession Number: PB90-121740

National Technical Report (Mitsushita Electric Industrial Company) Vol. 35, No. 4, August 1989. Special Issue: On Electronic Components and Devices for Information and Communication Equipments

Matsushita Electric Industrial Co. Ltd., Moriguchi (Japan).

Corp. Source Codes: 088966000

c1989 175p

Languages: Japanese

Journal Announcement: GRAI9007

Text in Japanese with English abstracts. See also PB90-121757 through PB90-121807 and PB89-227417. Portions of this document are not fully legible.

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NTIS Prices: PC A08/MF A01

... full keyboard for high speed operation; Printed wiring board for shielding electromagnetic waves; Low noise **power** supplies by regenerative **control** method 'RG Series'; **IC** cards; Microwave **transmit / receive** units for satellite communication; Indoor RF unit for satellite communication and phase noise characteristics; and...

38/3,K/28 (Item 3 from file: 6)

DIALOG(R)File 6:NTIS

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1222770 NTIS Accession Number: PB86-113180

Mitsubishi Denki Giho, Vol. 59, No. 8, 1985

Mitsubishi Electric Corp., Tokyo (Japan).

Corp. Source Codes: 076350000

c1985 78p

Languages: Japanese

Journal Announcement: GRAI8607

Text in Japanese with English abstracts. See also PB85-238426.

Order this product from NTIS by: phone at 1-800-553-NTIS (U.S. customers); (703)605-6000 (other countries); fax at (703)321-8547; and email at orders@ntis.fedworld.gov. NTIS is located at 5285 Port Royal Road, Springfield, VA, 22161, USA.

NTIS Prices: PC E04/MF E01

... Recognition Systems; A housekeeping system based on a home bus; Videotex Terminals; A Seam-Position **detector** for **electric**-resistance welded tubers and the model S-6500 Store-Data Processor. (Copyright (c) Mitsubishi Electric...

Descriptors: Microcomputers; **Integrated circuits**; Television receivers; Signal processing; Cathode ray tubes; Demodulators; Operational

February 21, 2003

amplifiers; **Transmitter receivers**

38/3,K/29 (Item 4 from file: 6)

DIALOG(R)File 6:NTIS

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0873848 NTIS Accession Number: AD-D007 977/2/XAB

High Power Microwave Integrated Circuit Receiver Protector with Integral Sensitivity Time Control

(Patent)

Gawronski, M. J. ; Goldie, H.

Department of the Air Force, Washington, DC.

Corp. Source Codes: 000260000; 109850

Report No.: PAT-APPL-6-055 423; PATENT-4 232 278

Filed 6 Jul 79 patented 4 Nov 80 8p

Languages: English Document Type: Patent

Journal Announcement: GRAI8109

Supersedes PAT-APPL-6-055 423, AD-D006 472.

This Government-owned invention available for U.S. licensing and, possibly, for foreign licensing. Copy of patent available Commissioner of Patents, Washington, DC 20231 \$0.50.

NTIS Prices: Not available NTIS

... base region diode functions as a quasi-active limiter with turn-on bias supplied by **detected RF current** in a Schottky barrier diode with a discharge resistor providing fast recovery; and the thinnest...

Descriptors: Patents; *Pin diodes; *Radar **transmitters** ; *Radar **receivers** ; Electronic switching; Microwave equipment; **Integrated circuits** ; High **power** ; **Control** systems; Sensitivity

38/3,K/30 (Item 1 from file: 34)

DIALOG(R)File 34:SciSearch(R) Cited Ref Sci

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03902974 Genuine Article#: QP775 No. References: 186

Title: LOW-POWER RADIOFREQUENCY ICS FOR PORTABLE COMMUNICATIONS

Author(s): ABIDI AA

Corporate Source: UNIV CALIF LOS ANGELES, DEPT ELECT ENGN/LOS ANGELES//CA/90024

Journal: PROCEEDINGS OF THE IEEE, 1995, V83, N4 (APR), P544-569

ISSN: 0018-9219

Language: ENGLISH Document Type: REVIEW (Abstract Available)

Abstract: The contributions of **integrated circuits** to the RF front-end of wireless **receivers** and **transmitters** operating in broadcast and personal communications bands are surveyed. It is seen from this that

...Identifiers-- **VOLTAGE - CONTROLLED OSCILLATOR**; **MOBILE TELEPHONE**; **TV TUNER**; **PERFORMANCE**; **TECHNOLOGY**; **AMPLIFIER**; **CHIP**; **CMOS**; **VHF**; **RECEIVER**